

MATRIX SWITCH with Single Chassis and MAXPRONET CPU

Overview

This application note introduces the use of a matrix switch utilizing the MAXPRONET CPU with the VideoBloX video switching hardware. This configuration uses the HVBGPIO for communications between the CPU and switching hardware as well as providing the PTZ control data.

The use of the HVBGPIO reduces the required hardware and cabling for communications between the CPU and the video switching hardware and control of PTZ devices.

The system diagrams show the difference between the new method using the HVBGPIO and the older method without the HVBGPIO.

This application note provides a sample application for use of the MAXPRONET CPU with VideoBloX switching hardware. Please consult your Honeywell sales professional for assistance with your specific application.

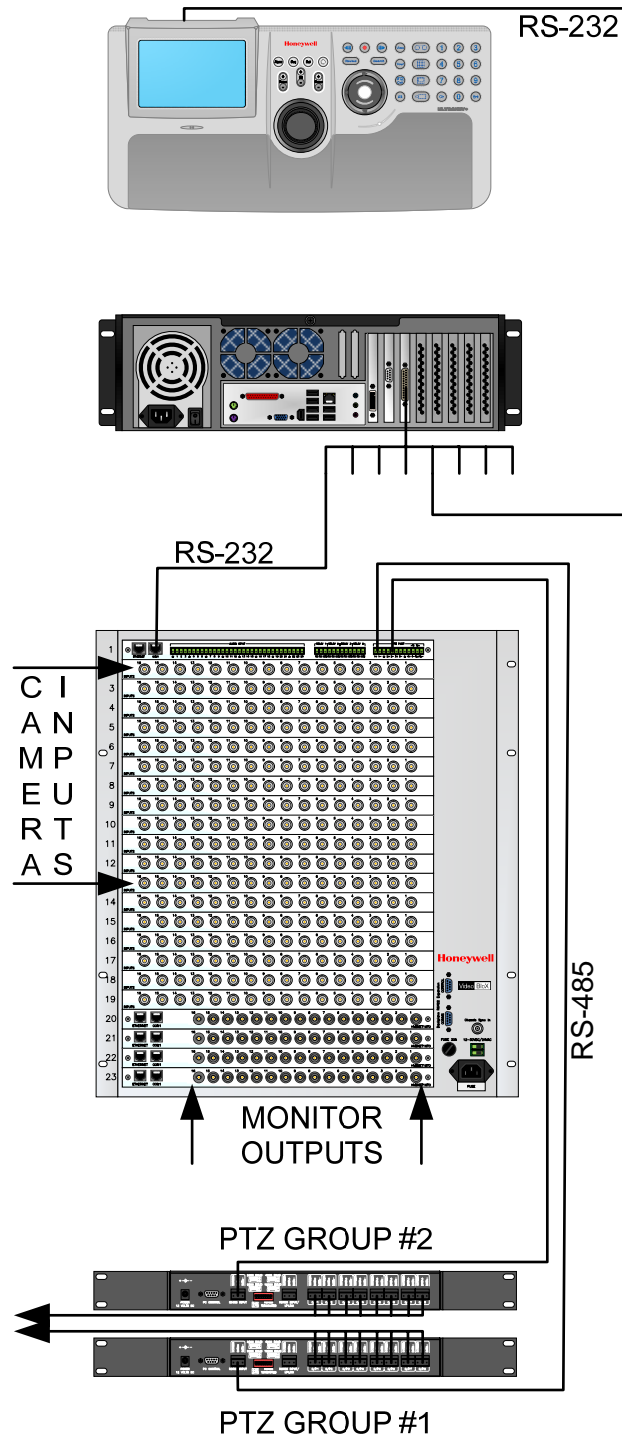
Model Numbers

- MAXPRONET3 – Matrix Switch CPU
- HVB4U, HVB8U or HVB12U Matrix Switch Chassis
- HVBGPIO General Purpose I/O Card
- HVBMxx Video Input Cards
- HVBNET16TO Monitor Output Card with Text Overlay
- HJK7000 Operator Joystick Controller
- HDCD8TP PTZ Data Distribution Device

Basic Installation Instructions

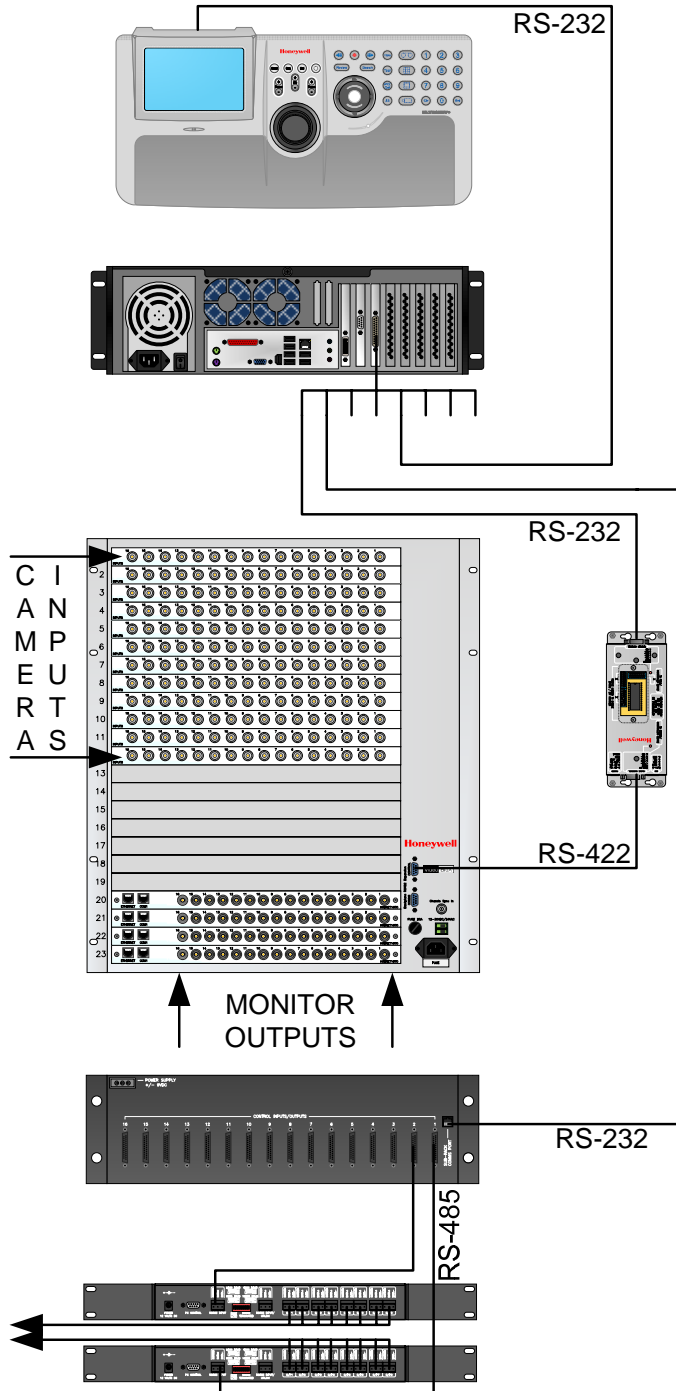
- Install MAXPRONET3 CPUs.
- Install Chassis' with cards.
- Install Operator Joystick Controller
- Power equipment.
- Program CPUs for operation

System Diagram #1 – New Method with HVBGPIO



4 PTZ Groups can have up to 128 PTZ devices connected each – PTZ Data distribution shown for 8 PTZ devices per channel.

System Diagram #2 – Previous Method without HVBGPIO



Up to 16 PTZ Groups can have up to 16 PTZ devices connected each – PTZ Data distribution shown for 8 PTZ devices per channel.

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