

MATRIX SWITCH with Two Chassis' and MAXPRONET CPU

Overview

This application note introduces the use of a matrix switch utilizing the MAXPRONET CPU with the VideoBloX video switching hardware. This note addresses the use of two video switching chassis' for increased numbers of video inputs. This configuration also uses the HVBGPIO for communications between the CPU and switching hardware as well as providing the PTZ control data.

The use of the HVBGPIO reduces the required hardware and cabling for communications between the CPU and the video switching hardware and control of PTZ devices.

The system diagrams show the use of two HVBGPIO cards for communications with the CPU and PTZ control and the use of one HVBGPIO for communications with the CPU where a lesser number of PTZ control lines are required.

This application note provides a sample application for use of the MAXPRONET CPU with VideoBloX switching hardware. Please consult your Honeywell sales professional for assistance with your specific application.

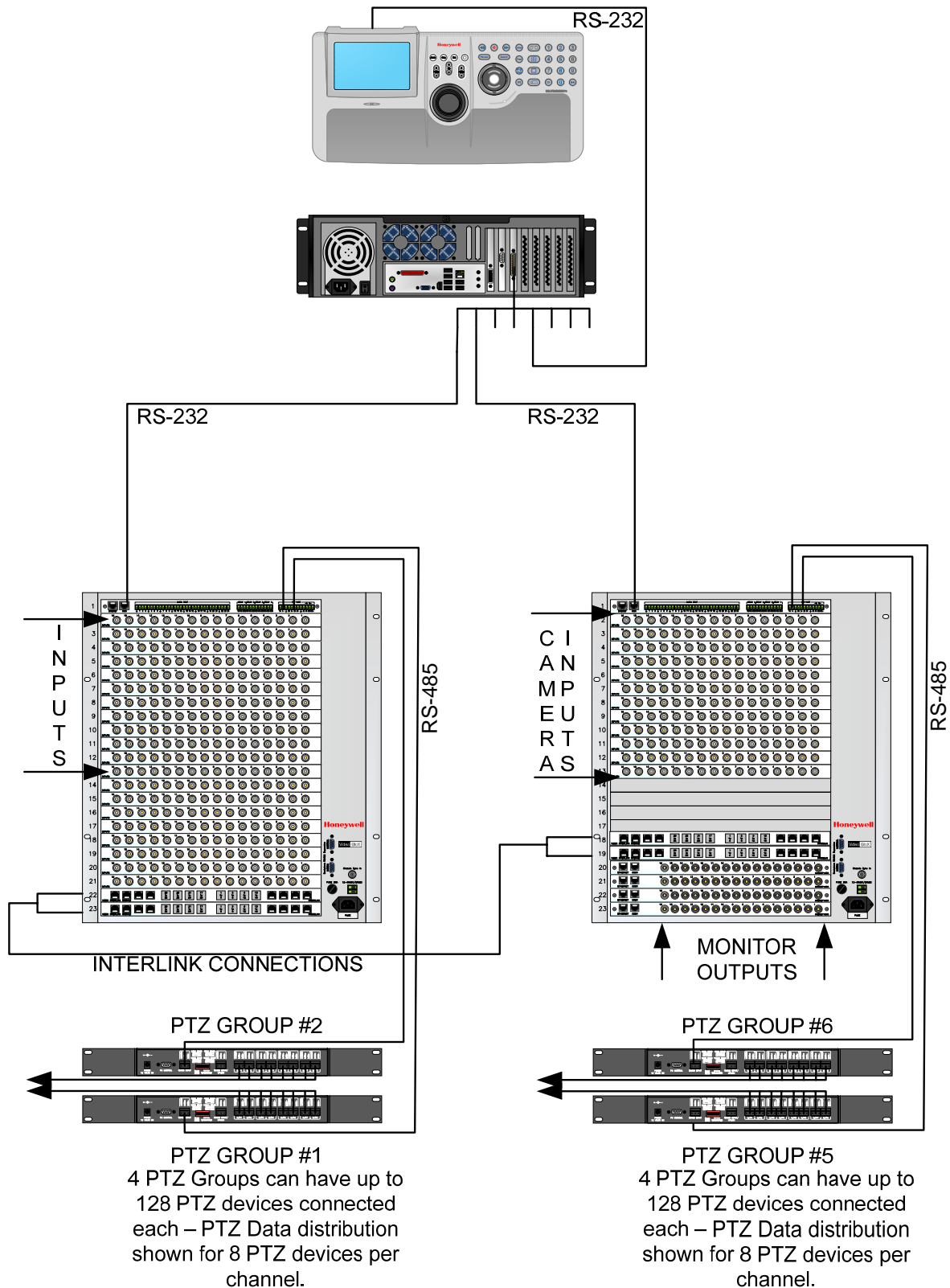
Model Numbers

- MAXPRONET3 – Matrix Switch CPU
- HVB4U, HVB8U or HVB12U Matrix Switch Chassis (x2)
- HVBGPIO General Purpose I/O Card
- HVBMxx Video Input Cards
- HVBNET16TO Monitor Output Card with Text Overlay
- HJK7000 Operator Joystick Controller
- HDCD8TP PTZ Data Distribution Device

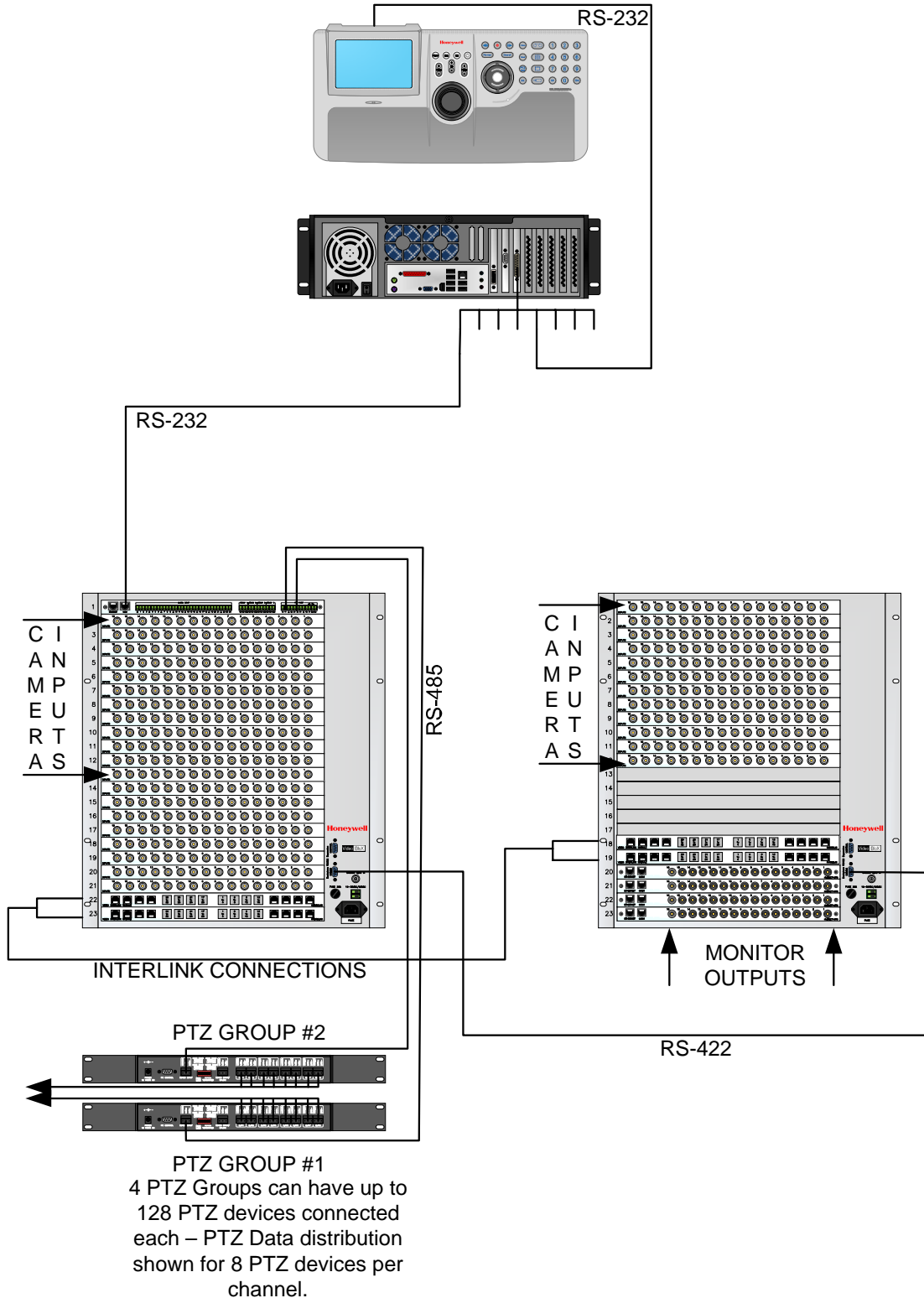
Basic Installation Instructions

- Install MAXPRONET3 CPUs.
- Install Chassis' with cards.
- Install Operator Joystick Controller
- Power equipment.
- Program CPUs for operation

System Diagram #1 – Two Chassis' with Two HVBGPIO



System Diagram #2 – Two Chassis' with One HVBGPIO



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