

Redundant MAXPRONET CPUs

Overview

This application note introduces the ability to operate a matrix switch with redundant CPUs to increase operational reliability.

The use of a redundant matrix switch CPU allows new programming to be configured on the secondary CPU, while running the system on the primary CPU. Configuration changes can be applied by making the secondary CPU a temporary master, the primary CPU is left in a standby mode in case issues arise with the new configuration. Once programming changes are tested and verified, the changes can be applied to the primary CPU with both CPUs in full operation.

This application note provides a sample application for redundant CPUs on a matrix switch. Please consult your Honeywell sales professional for assistance with your specific application.

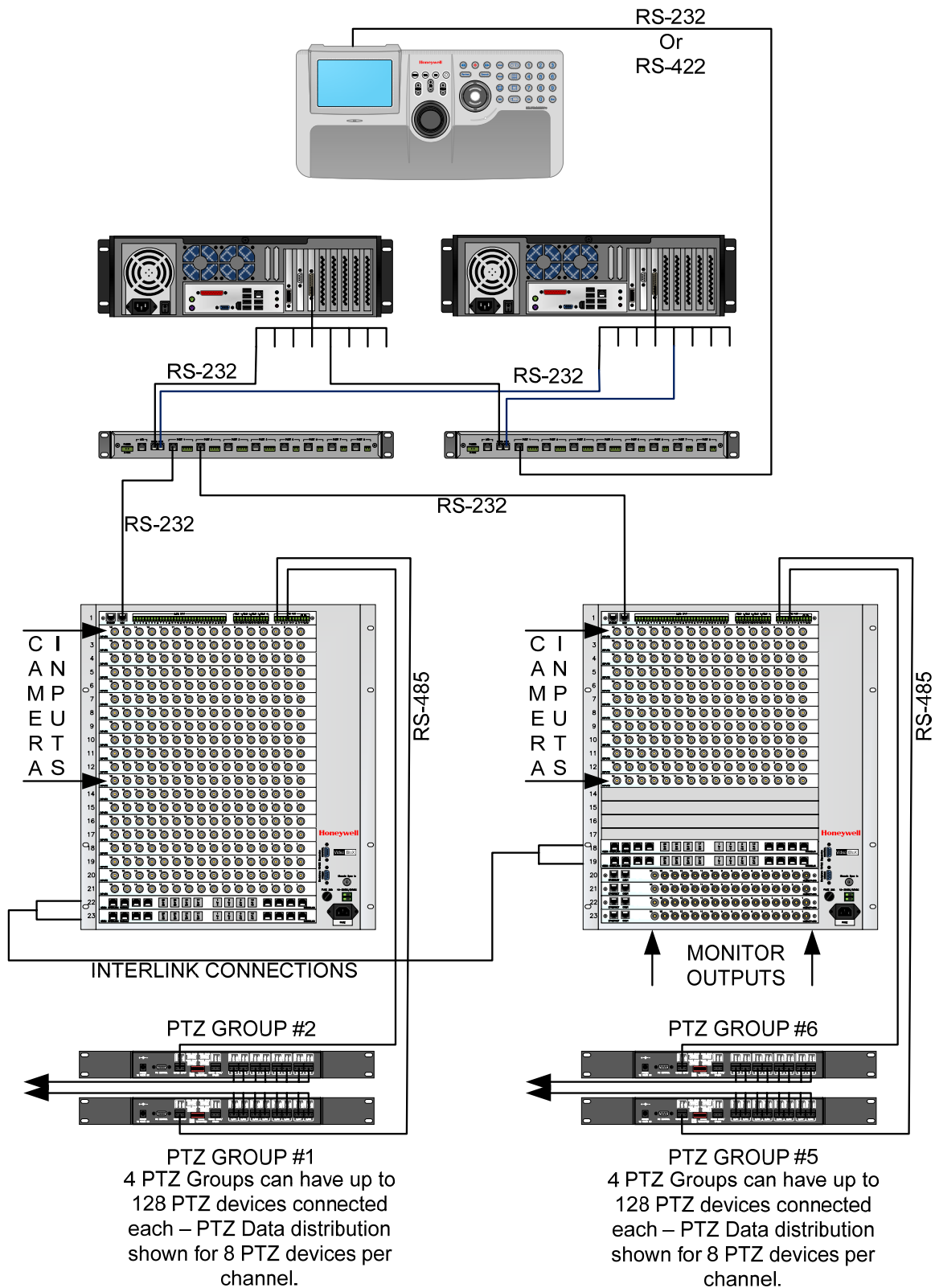
Model Numbers

- MAXPRONET3 – Matrix Switch CPU (x2)
 - HS10 PIT – Data Distribution and Data Fail-Over Units
 - HVB4U, HVB8U or HVB12U Matrix Switch Chassis
 - HVBGPIO General Purpose I/O Card
 - HVBMxx Video Input Cards
 - HVBNET16TO Monitor Output Card with Text Overlay
 - HVB32LKI and HVB32LKO Interlink Cards
 - HJK7000 Operator Joystick Controller
 - HDCD8TP PTZ Data Distribution Device
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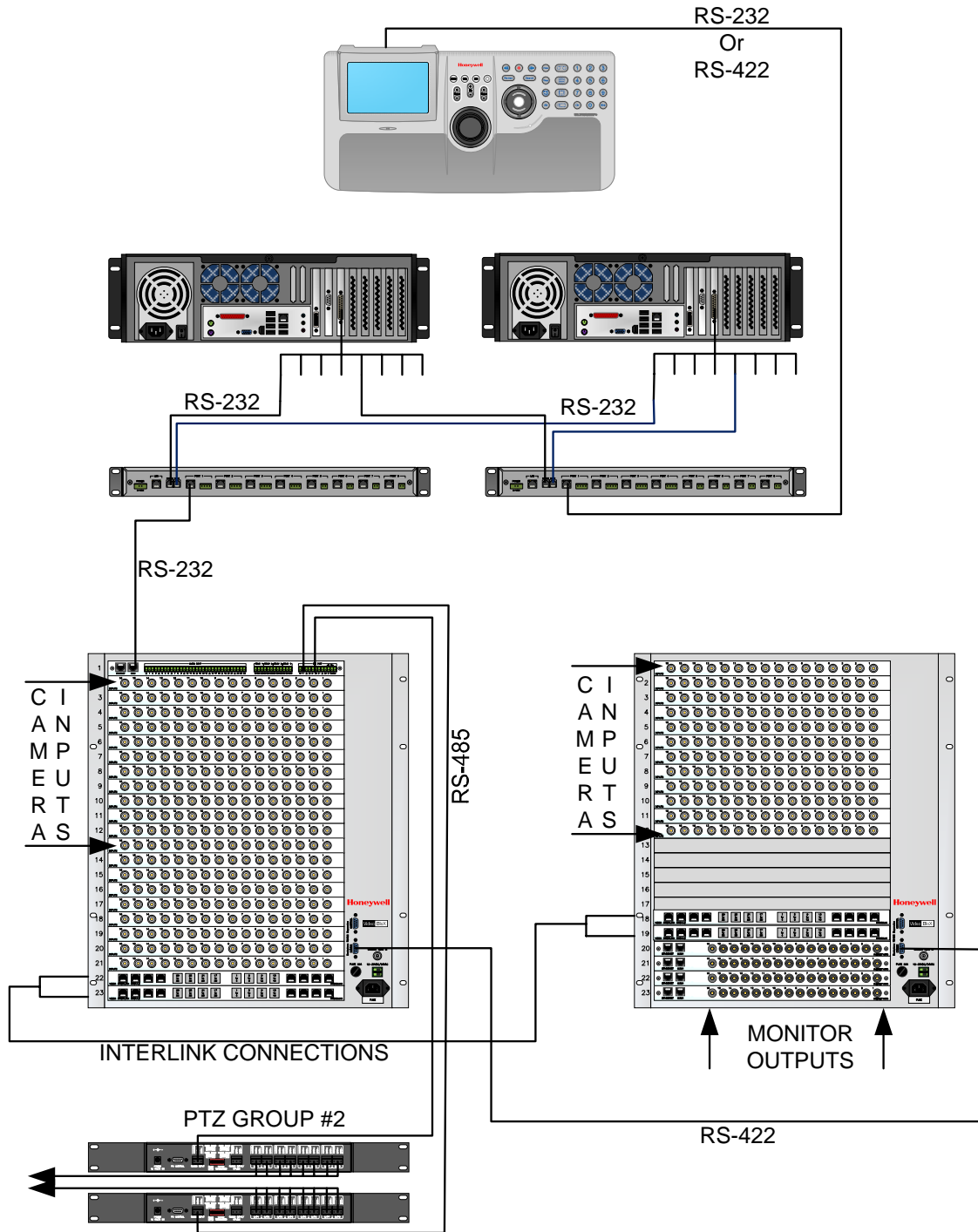
Basic Installation Instructions

- Install MAXPRONET3 CPUs.
- Install HS10PIT units
- Install Chassis' with cards.
- Install Operator Joystick Controller
- Power equipment.
- Program CPUs for operation

System Diagram #1



System Diagram #2



PTZ GROUP #1
 4 PTZ Groups can have up to 128 PTZ devices connected each – PTZ Data distribution shown for 8 PTZ devices per channel.

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